CLAIM AMENDMENTS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A method for supplying a clock signal to processor-controlled apparatuses, which comprises:

generating a quartz frequency with a clock quartz;

providing a clock frequency based on ~~a~~ the quartz frequency of ~~a~~ the clock quartz to a device for determining a clock time; ~~and~~

providing the clock frequency to a processor device during operational time periods of low processor load or no processor load and otherwise providing a different clock frequency to the processor device;

switching the processor device to a clockless state during operational time periods with no processor load; and

selecting the clock signal as a function of processor load according to the following:

during operational time periods with low processor load or no processor load, providing the same clock signal to the processor device as to the device for determining a clock time, and

during operational time periods with processor loading, providing a clock signal based on a system clock to the processor device.

Claim 2-4 (canceled).

Claim 5 (currently amended): The method according to claim ~~2~~ 1, which further comprises clocking the processor device with a reduced frequency during operational time periods with low processor load, the reduced frequency being lower than the frequency of the system clock and higher than the quartz frequency or the frequency derived therefrom.

Claim 6 (original): The method according to claim 1, which further comprises initiating, with the processor device, a selection of a clock frequency to be fed to the processor device, being lower than a current frequency fed to the processor device.

Claim 7 (original): The method according to claim 1, which further comprises initiating, with the processor device, a selection of a clock frequency to be fed to the processor device, being higher than a current clock frequency fed to the processor device.

Claim 8 (original): The method according to claim 1, which further comprises initiating, with external events, a selection of a clock frequency to be fed to the processor device, being higher than a current clock frequency fed to the processor device.

Claim 9 (original): The method according to claim 1, which further comprises initiating, after expiration of a predefined time period, a selection of a clock frequency to be fed to the processor device, being higher than a current clock frequency fed to the processor device.

Claim 10 (original): The method according to claim 1, which further comprises temporarily switching off not-required components of an apparatus as a function of the clock frequency fed to the processor device.

Claim 11 (previously presented): In a configuration for supplying a clock signal to processor-controlled apparatuses

having a processor device and associated with a device for determining a clock time, the improvement comprising:

a clock selector unit connected to the processor device for selecting a frequency to be fed to the processor device, as a function of a processor load;

an oscillator having a clock quartz for generating a quartz frequency, the oscillator being configured to feed a clock frequency based on the quartz frequency or a frequency derived therefrom to the device for determining the clock time;

said clock selector unit feeding a clock frequency based on the quartz frequency or on a frequency derived therefrom to the processor device when there is no processor load or when there is low processor load; and

otherwise the processor device being clocked with a system clock.

Claim 12 (previously presented): A method for conserving power of a processor device of a processor-controlled apparatus operating by a clock supply system driven according to a usage factor, the method which comprises the steps of:

generating a quartz frequency as a function of a quartz clock;

providing the quartz frequency to a real-time clock;

selecting a clock frequency as a function of processor load;

generating a clock signal based on the selected clock frequency; and

providing the generated clock signal to the processor device.

Claim 13 (previously presented): The method according to claim 12, wherein the clock frequency is selected from the group consisting of a quartz clock frequency, a real-time clock frequency, a standby clock frequency, and a system clock frequency.

Claim 14 (previously presented): The method according to claim 12, wherein the step of selecting a clock frequency as a function of processor load, selects the same quartz frequency as provided to the real-time clock when there is no processor load.

Claim 15 (previously presented): The method according to claim 12, wherein the step of selecting a clock frequency as a function of processor load, selects the same quartz frequency as provided the real-time clock when there is low processor load.

Claim 16 (previously presented): The method according to claim 12, wherein the step of selecting a clock frequency as a function of processor load is based in part on received processor control signals, interrupt control signals, and timer control signals.